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(71) Applicant (for DE only): **PHILIPS INTELLECTUAL
PROPERTY & STANDARDS GMBH** [DE/DE]; Stein-
damm 94, 20099 Hamburg (DE).

(71) Applicant (for all designated States except DE, US):
KONINKLIJKE PHILIPS ELECTRONICS N. V.
[NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven
(NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **BRILKA, Joachim**

[DE/DE]; c/o Philips Intellectual Property & Standards
GmbH, Weisshausstr. 2, 52066 Aachen (DE). **KAT-
TNER, Axel** [DE/DE]; c/o Philips Intellectual Property
& Standards GmbH, Weisshausstr. 2, 52066 Aachen
(DE). **RAGOSCH, Ernst-Peter** [DE/DE]; c/o Philips
Intellectual Property & Standards GmbH, Weisshausstr. 2,
52066 Aachen (DE).

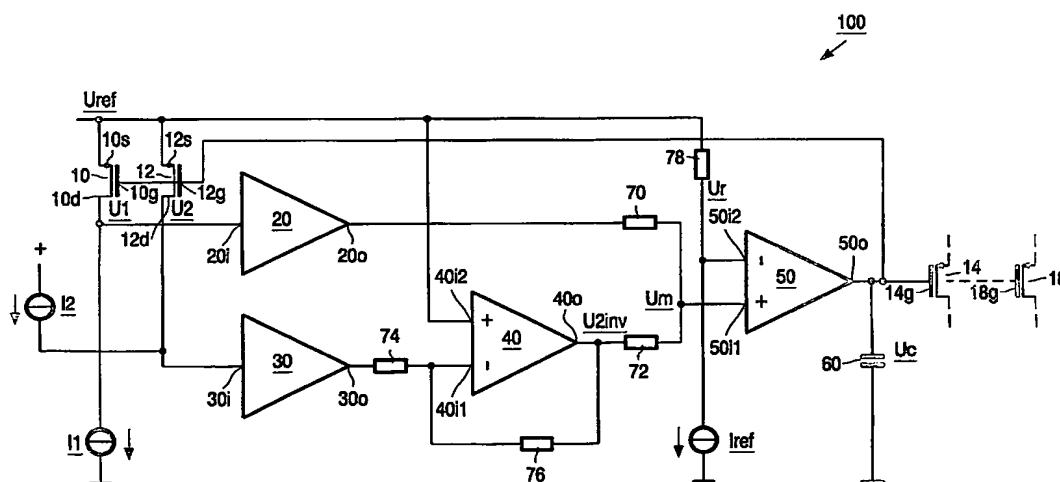
(74) Agent: **VOLMER, Georg**; Philips Intellectual Property &
Standards GmbH, Weisshausstr. 2, 52066 Aachen (DE).

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(54) Title: **CIRCUIT ARRANGEMENT AND TRANSISTOR CONTROL METHOD**



(57) Abstract: In order to improve a circuit arrangement (100) and a method of controlling at least one transistor (10, 12, 14, , 18), especially of controlling the resistance value of at least one MOS transistor with vanishing DC modulation in such a way that a compensation of resistance variations without control deviation is also possible for the case where the transistor (10 , 12 , 14 , , 18) is operated with a vanishing DC voltage, i.e. with a zero DC voltage and indeed without the aid of a reference frequency, it is suggested that in addition to at least a first reference element (10, 20, 70), which has at least a first reference transistor (10) with a first offset from the operating point, at least a second reference element (12, 30, 40, 72, 74, 76) is provided which has at least a second reference transistor (12) with a second offset from the operating point equal in value but opposed in sign to the first buffer storage, wherein an in particular arithmetic average can be formed from the first offset and the second offset for approximating and achieving an optimum operating point.



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